

## STP6NB25 STP6NB25FP

N-CHANNEL 250V - 0.9Ω - 6A TO-220/TO-220FP PowerMesh<sup>TM</sup> MOSFET

TYPE	V <sub>DSS</sub>	R <sub>DS(on)</sub>	I <sub>D</sub>
STP6NB25	250 V	< 1.1 Ω	6 A
STP6NB25FP	250 V	< 1.1 Ω	3.7 A

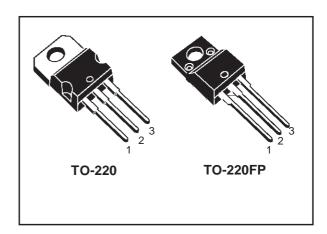
- TYPICAL  $R_{DS}(on) = 0.9 \Omega$
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- VERY LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED

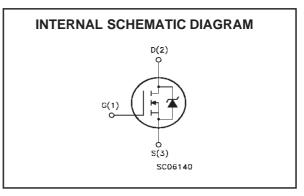
#### **DESCRIPTION**

Using the latest high voltage MESH OVERLAYTM process, STMicroelectronics has designed an advanced family of power MOSFETs with outstanding performances. The new patent pending strip layout coupled with the Company's proprieraty edge termination structure, gives the lowest  $R_{\text{DS}(\text{on})}$  per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

#### **APPLICATIONS**

- HIGH CURRENT, HIGH SPEED SWITCHING
- UNINTERRUPTIBLE POWER SUPPLY (UPS)
- DC-DC & DC-AC CONVERTERS FOR TELECOM, INDUSTRIAL AND CONSUMER ENVIRONMENT





#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Value		Unit		
		STP6NB25	STP6NB25FP			
V <sub>DS</sub>	Drain-source Voltage (V <sub>GS</sub> = 0)	250	)	V		
V <sub>DGR</sub>	Drain-gate Voltage ( $R_{GS} = 20 \text{ k}\Omega$ )	250	)	V		
V <sub>GS</sub>	Gate- source Voltage	±30		±30		V
I <sub>D</sub>	Drain Current (continuos) at T <sub>C</sub> = 25°C	6 3.7		А		
I <sub>D</sub>	Drain Current (continuos) at T <sub>C</sub> = 100°C	3.8 2.3		А		
I <sub>DM</sub> (•)	Drain Current (pulsed)	24 24		Α		
P <sub>TOT</sub>	Total Dissipation at T <sub>C</sub> = 25°C	75	30	W		
	Derating Factor	0.6	0.24	W/°C		
dv/dt (1)	Peak Diode Recovery voltage slope	5.5		V/ns		
V <sub>ISO</sub>	Insulation Withstand Voltage (DC)	- 2000				
T <sub>stg</sub>	Storage Temperature	-60 to 150		°C		
Tj	Max. Operating Junction Temperature	150		°C		

(•)Pulse width limited by safe operating area Jun 2000

#### STP6NB25/FP

#### THERMAL DATA

		TO-220	TO-220FP	
Rthj-case	Thermal Resistance Junction-case Max	1.66 4.17		°C/W
Rthj-amb	Thermal Resistance Junction-ambient Max	62.5		°C/W
Rthc-sink	Thermal Resistance Case-sink Typ	0.5		°C/W
Tı	Maximum Lead Temperature For Soldering Purpose	300		°C

#### **AVALANCHE CHARACTERISTICS**

Symbol	Parameter	Max Value	Unit
I <sub>AR</sub>	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by $T_j$ max)	6	А
E <sub>AS</sub>	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = I_{AR}$ , $V_{DD} = 50$ V)	200	mJ

# **ELECTRICAL CHARACTERISTICS** (TCASE = 25 $^{\circ}$ C UNLESS OTHERWISE SPECIFIED) OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>(BR)DSS</sub>	Drain-source Breakdown Voltage	$I_D = 250 \mu\text{A},  V_{GS} = 0$	600			V
I <sub>DSS</sub>	Zero Gate Voltage	V <sub>DS</sub> = Max Rating			1	μΑ
פטי	Drain Current (V <sub>GS</sub> = 0)	V <sub>DS</sub> = Max Rating, T <sub>C</sub> = 125 °C			50	μΑ
I <sub>GSS</sub>	Gate-body Leakage Current (V <sub>DS</sub> = 0)	V <sub>GS</sub> = ±30V			±100	nA

#### ON (1)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	2	3	4	V
R <sub>DS(on)</sub>	Static Drain-source On Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> =3 A		0.9	1.1	Ω
I <sub>D(on)</sub>	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max},$ $V_{GS} = 10V$	6			А

#### **DYNAMIC**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
g <sub>fs</sub> (1)	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max},$ $I_{D} = 3A$		3		S
C <sub>iss</sub>	Input Capacitance			260		pF
Coss	Output Capacitance	V <sub>DS</sub> = 25V, f = 1 MHz, V <sub>GS</sub> = 0		68		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	55 7 7 111112, 100 1		9		pF

#### **ELECTRICAL CHARACTERISTICS** (CONTINUED)

#### **SWITCHING ON**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t <sub>d(on)</sub>	Turn-on Delay Time	$V_{DD} = 125 \text{ V}, I_D = 3 \text{ A}$		9		ns
t <sub>r</sub>	Rise Time	$R_G = 4.7\Omega V_{GS} = 10 V$ (see test circuit, Figure 3)		9		ns
Qg	Total Gate Charge			12	17	nC
Q <sub>gs</sub>	Gate-Source Charge	$V_{DD} = 200V, I_D = 6 A,$ $V_{GS} = 10V$		7.5		nC
Q <sub>gd</sub>	Gate-Drain Charge	1.00		3		nC

#### **SWITCHING OFF**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
$t_{r(Voff)}$	Off-voltage Rise Time	V <sub>DD</sub> = 200V, I <sub>D</sub> = 6 A,		8		ns
t <sub>f</sub>	Fall Time	$R_G = 4.7\Omega$ , $V_{GS} = 10V$		7		ns
t <sub>c</sub>	Cross-over Time	(see test circuit, Figure 5)		15		ns

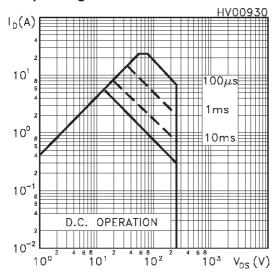
#### SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Isd	Source-drain Current	ource-drain Current			6	Α
I <sub>SDM</sub> (2)	Source-drain Current (pulsed)				24	Α
V <sub>SD</sub> (1)	Forward On Voltage	I <sub>SD</sub> = 6 A, V <sub>GS</sub> = 0			1.6	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> = 6 A, di/dt = 100A/μs,		160		ns
Q <sub>rr</sub>	Reverse Recovery Charge	$V_{DD} = 100V, T_j = 150^{\circ}C$		720		μС
I <sub>RRM</sub>	Reverse Recovery Current	(see test circuit, Figure 5)		9		А

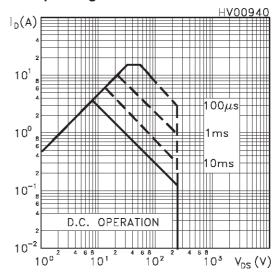
Note: 1. Pulsed: Pulse duration = 300  $\mu$ s, duty cycle 1.5 %.

2. Pulse width limited by safe operating area.

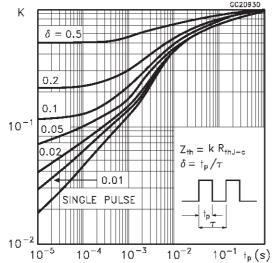
#### Safe Operating Area for TO-220

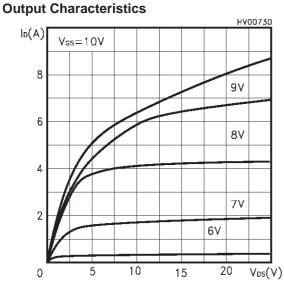


#### Safe Operating Area for TO-220FP

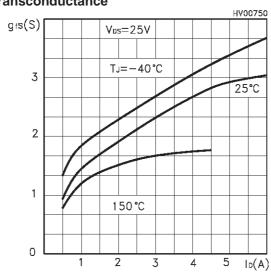


#### Thermal Impedence for TO-220

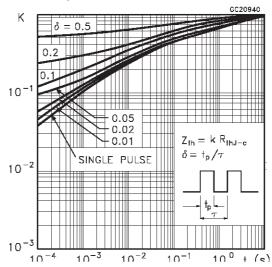




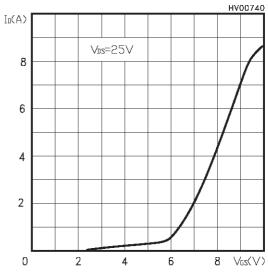
### **Transconductance**



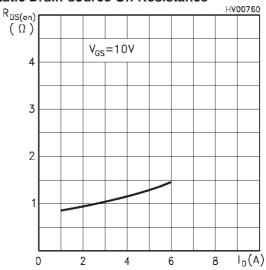
#### Thermal Impedence for TO-220FP



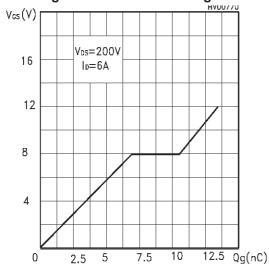
#### **Transfer Characteristics**



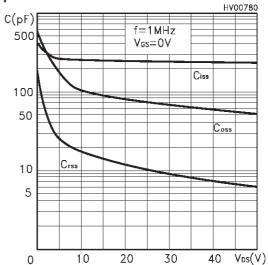
#### **Static Drain-source On Resistance**



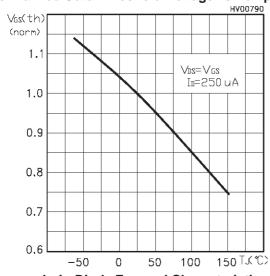
#### **Gate Charge vs Gate-source Voltage**



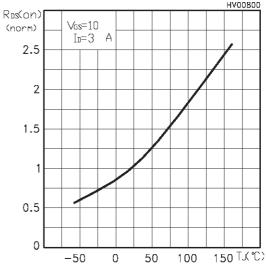
#### **Capacitance Variations**



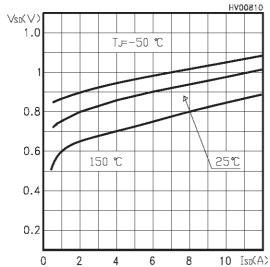
#### Normalized Gate Threshold Voltage vs Temp.



#### Normalized On Resistance vs Temperature



#### **Source-drain Diode Forward Characteristics**



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Fig. 1: Unclamped Inductive Load Test Circuit

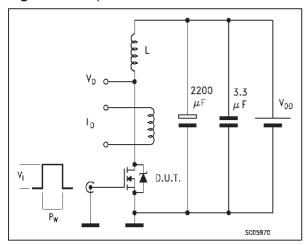


Fig. 3: Switching Times Test Circuit For Resistive Load

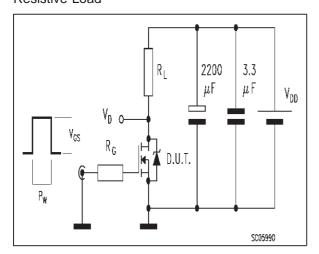


Fig. 5: Test Circuit For Inductive Load Switching And Diode Recovery Times

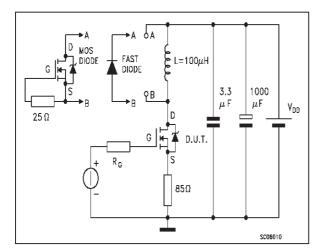


Fig. 2: Unclamped Inductive Waveform

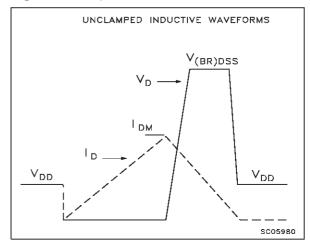
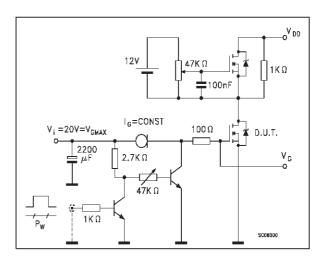
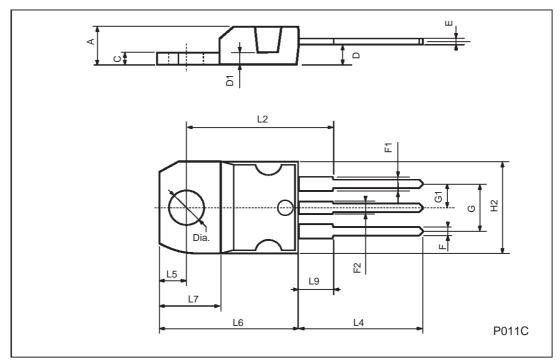


Fig. 4: Gate Charge test Circuit



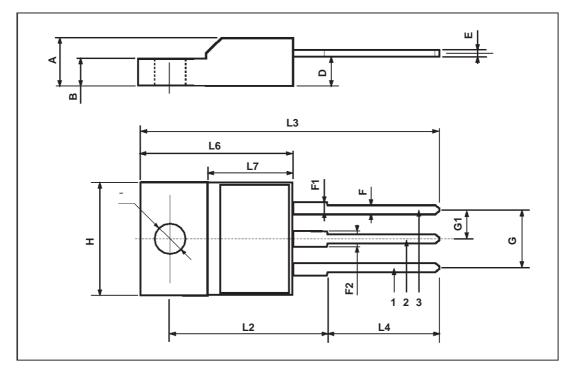
#### **TO-220 MECHANICAL DATA**

DIM.		mm			inch	
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
Е	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



TO-220FP	MECHANIC	ΔΙ ΝΔΤΔ

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



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